



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

Opp : Yerragattu Gutta, Hasanparthy (Mandal), WARANGAL - 506 015, Telangana, INDIA.

కాకతీయ ప్రేయోగికీ एवं विज्ञान संस्थान, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారత

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - ౫౦౬ ౦౧౫ తెలంగాణ, భారతదేశము

(An Autonomous Institute under Kakatiya University, Warangal)

(Approved by AICTE, New Delhi; Recognised by UGC under 2(f) & 12(B); Sponsored by EKASILA EDUCATION SOCIETY)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
CLASSWORK TIMETABLE for the ODD SEMESTER of Academic Year 2021-22

I SEMESTER of M. Tech (ES&VLSI)

Class Teacher : Dr.K.Sowjanya

Classroom : B-VI-208

w.e.f 13.12.2021

Day	9.40-10.30	10.30-11.20	11.20-12.10	12.10-13.00	LUNCH BREAK	14.00-14.50	14.50-15.40	15.40-16.30	
Monday	MES	SVDV	DICD	ESD		<-----MES-Lab-----> Dr. KS/KSJ			
Tuesday	DICD	ERPW	SVDV	ESD		RMIPR	MES	-	
Wednesday	SVDV	RMIPR	ERPW	MES		ESD	DICD	-	
Thursday	DICD(T)	ESD(T)	ERPW(T)	SVDV(T)		<-----DICD-Lab-----> BJ/GR			
Friday	MES(T)	<-----MES-Lab-----> Dr. KS/KSJ				<-----DICD-Lab-----> BJ/GR			
Saturday	-	-	-	-					

Course Title	Course Code & Course Name	Course Faculty
DICD	: P20EV101-Digital IC Design	- B. Jeevan (BJ)
MES	: P20SP102-Microcontroller based Embedded Systems	- Dr. K. Srinivas (KS)
SVDV	: P20EV103B- System Verilog for Design and Verification	- Dr. CH. Sridevi(CHS)
ESD	: P20EV104A- Embedded System Design	- K. Shailaja(KSJ)
RM&IPR	: P20MC107- Research Methodology & IPR	- Dr. K. Sowjanya (KS)
ERPW	: P20AC108A-English for Research Paper Writing	- B. Shamsikanth (BSK)
DICD-Lab	: P20EV105- Digital IC Design Laboratory	- B. Jeevan/ G. Raju
MES-Lab	: P20EV106- Microcontroller based Embedded Systems Laboratory	- Dr. K. Srinivas/ K.Shailaja

Sd/-
Faculty I/c,
Dept.Timetables

Sd/-
Faculty I/c,
Overall Timetables

Sd/-
Head of the
Department

Sd/-
Dean, AA

Sd/-
Principal